

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	8161660
<b>Application Number:</b>	10719020
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9168
<b>Title of Invention:</b>	METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER
<b>First Named Inventor/Applicant Name:</b>	Akira Matsuda
<b>Customer Number:</b>	38834
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<b>Filer Authorized By:</b>	Stephen G. Adrian
<b>Attorney Docket Number:</b>	032130
<b>Receipt Date:</b>	05-AUG-2010
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 180
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed (SB/08)	032130_IDS_Form_SB08_08-05 -2010.pdf	763173  66aa539f8a1091bc4b94d3a082cda5cc021 a3fd8	no	4
<b>Warnings:</b>					
<b>Information:</b>					
2	Fee Worksheet (PTO-875)	fee-info.pdf	30658  6732fe74b690c902415ea0de0dc1a5a7b1e 89814	no	2
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			793831		
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